SIEMENS

Course Description

Calibre
Calibre Using PERC

Course Code	249505
Software	Calibre 2026.2
User Level	All
PricingID	Contact Us
Live In-Person Duration	1 day

Course Overview

The Calibre Using PERC course will help you understand the wide breadth of problem areas addressed by Calibre PERC, including ESD, advanced ERC, multiple power domains, layout point-to-point resistance, and layout current density. The course will introduce you to this powerful tool and provide a basic understanding of setting up and running Calibre PERC jobs. Calibre PERC concepts will be demonstrated through a collection of detailed case studies. Students will be able to explore Calibre PERC job options and debugging techniques through the use of hands-on lab exercises. The course includes a Calibre overview module for those students not familiar with Calibre applications.

Course Details

Reliability is a growing concern for integrated circuit designers. In response to this growing need, Siemens EDA has developed Calibre PERC to address reliability challenges that arise during the circuit and electrical verification process. PERC is specifically designed to perform electrostatic discharge (ESD) and multiple power domain checks. Calibre PERC allows you to customize ERC checks at the schematic level as well as geometrical and electrical checks at the layout, which gives you more power and flexibility to handle emerging circuit verification demands for design implementation.

PREREQUISITES

PRIMARY COURSE TOPICS

Prerequisites

- Familiarity with basic electronic circuit design techniques
- Some knowledge of the Spice netlist format
- Experience with typical circuit Electrical Rule Checks (ERC) and/or Electrostatic
- Discharge (ESD) protection is helpful but not required
- Experience using Unix or Linux is helpful but not required

You Will Learn How To

- Create job-specific Calibre PERC control files.
- Invoke Calibre PERC from the command line and from the interactive GUI.
- Open Calibre PERC results in Calibre RVE.
- Use Calibre RVE to highlight PERC results in netlist, schematic, and layout views.
- Apply Calibre PERC to typical ERC and ESD design scenarios.
- Analyze both Spice netlists and chip layout data.

Hands-On Labs

Throughout this course, extensive hands-on lab exercises provide you with practical experience using Calibre PERC software. Hands-on lab topics include: * Command line invocation of Calibre PERC * Using the Calibre PERC interactive GUI for job setup and execution * Creating and

editing PERC job control files * Highlighting PERC results using Calibre RVE