

Design Verification with Simulink

OEM: MathWorks • Duration: 1 Day (8 hrs) • Code: MW-DESIGN-VERIFICATION-WITH-SIMULINK

COURSE MODULES & TOPICS

Day 1 of 1

Understanding the Verification Workflow

- Introducing formal verification methods
- Exploring the Simulink Design Verifier workflow
- Preparing models and ensuring compatibility
- Configuring analysis options
- Generating results

Detecting Design Errors

- Introducing types of design errors
- Detecting design errors
- Interpreting results and reporting
- Fixing design errors
- Using parameter tables

Automatically Generating Tests

- Understanding types of coverage
- Measuring model coverage
- Increasing coverage with automatic test generation
- Interpreting results and reporting
- Applying custom conditions and objectives

Property Proving

- Formalizing requirements
- Proving properties
- Debugging falsified properties
- Using temporal operators

Managing Model Complexity

- Identifying sources of model complexity
- Implementing block replacements
- Handling nonlinearities

- Managing lookup table complexity
- Working with counters and timers